

I²C Bus Analyzer Feature Comparison

I ² C Bus Analyzer Summary	BusPro-I	CAS-1000-I2C/E
I²C Feature Summary		
Part Number	90010	90002
Monitor (I ² C Traffic Capture—Up to 5 MHz)	✓	✓
Debugger (I ² C Master—Up to 5 MHz)	✓	✓
Programmer (In-System Programming)	✓	✓
Emulator (I ² C Slave Device Emulation—Up to 1.9 MHz)		✓
Test (Script-Driven I ² C Bus Tester)		✓
Parameters Scope (I ² C Bus Measurement)		✓

Specifications	BusPro-I	CAS-1000-I2C/E
General		
High-speed, Bus-powered USB 2.0 Device	✓	✓
I ² C Exerciser Software GUI	✓	✓
Microsoft Windows Vista, 7, 8.1, and 10 Support	✓	✓
RoHS Compliant, CE Marked	✓	✓
Third Party Interface		
Development API / DLL	✓	✓
LabVIEW, Python, and Visual Basic Support	✓	✓
I²C Bus Support		
Standard-mode, Fast-mode, Fast-mode Plus (Fm+), and High-speed mode (Hs-mode)	✓	✓
7-bit & 10-bit Address Support	✓	✓
Bus Master, Multi-Master Capable	✓	✓
PEC (Packet Error Checking) Generation & Validation	✓	✓
Bus Electrical Support		
Programmable Reference Voltage Drive (0.8V to 5V)	✓	✓
Separate Programmable High/Low Thresholds Supporting Hysteresis	✓	✓
Programmable Pull-ups (250 to 50K ohms)	✓	✓
Programmable Accelerated Rising Edge Slope Control	✓	✓
External Signaling		
Two General Purpose I/O Signals	✓	✓
SMB Trigger In	✓	✓
SMB Trigger Out	✓	✓
IEEE-1149.1 Boundary-Scan JTAG Controller		✓

Software Module Detail	BusPro-I	CAS-1000-I2C/E
Traffic Monitor		
Trace Listing with Time Stamp	✓	✓
Timing Waveform Depiction	✓	✓
Programmable Traffic Filtering & Symbol Replacement	✓	✓
Advanced Trigger Event Detection	✓	✓
Automatic Protocol Checking	✓	✓
Continuous Logging of Trace Data	✓	✓
Debugger		
GUI-based Debugging	✓	✓
Serial EEPROM Programmer		
Read, Erase, Program, & Verify	✓	✓
Device Emulator		
Emulated Master Device		✓
Emulated Slave Device (Up to 10)		✓
Emulated Slave Clock Stretching on Data ACK Bit		✓
Script-driven Bus Tester		
Script-based Debugging		✓
Forced Bus Error Injection		✓
Glitch Pattern Injection		✓
Parameters Scope		
Analog Waveform Display		✓
Parametric Measurements		✓
Target Reference Voltage Measurement		✓
Target Capacitance Measurement		✓
Target Pull-up Resistance Measurement		✓
Target Rise/Fall Times Measurement		✓
Target Setup/Hold Times Measurement		✓
Target Device Slave Suite Detection		✓
Target Edge Analog Waveform Capture at Protocol Points		✓
Validate Most Bus Specification Characteristics		✓

13100 Alondra Blvd.
Cerritos, California 90703

Tel: (562) 926-6727
Fax: (562) 404-6196

Email: sales@corelis.com
Web: www.corelis.com

Corelis, the Corelis Logo, CAS-1000, and BusPro-I are trademarks of Corelis, Inc. Product names, logos, brands, and other trademarks featured or referred to are the property of their respective trademark holders. Corelis, Inc. reserves the right to make changes in design or specification at any time and without notice.
I2C-DS VERSION 1.0—2/17/2017